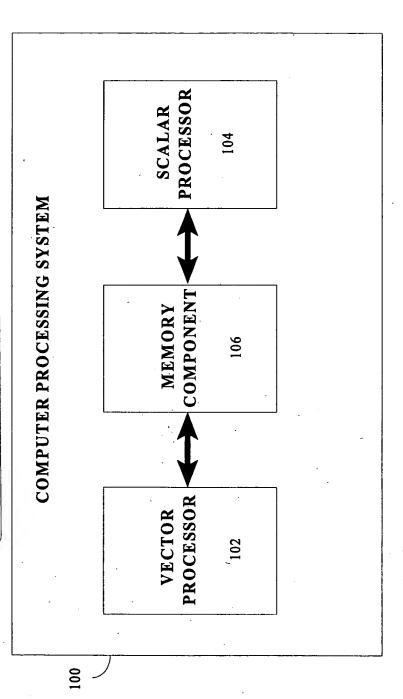
APPLN, FILING DATE: WARCH'21, 2001 THE FIGURE TITLE: MATRIX MULTIPLICATION IN A VECTOR PROCESSING SYSTEM

INVENTOR(S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED

SHEET 1 of 15



FIGURE

APPLN. FILING DATE: MARCH 21, 2001 TITLE: MATRIX MULTIPLICATION IN A SECTION OF

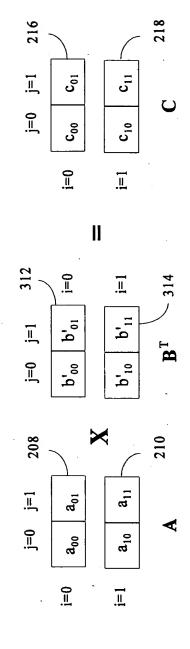
INVENTOR(S): ALI SAZEGARI PROCESSING SYSTEM

APPLICATION SERIAL NO: UNASSIGNED

SHEET 2 of 15

218 216 <u>j=1</u> c_{11} ဝ <u>j=0</u> \mathbf{c}^{10} ပိ <u>=</u>0 i=1 212 j=0 <u>i=1</u> 214 \mathbf{b}_{11} <u>j=1</u> \mathbf{b}_{01} 8 j=0 \mathbf{b}_{10} **6**00 × 208 210 **a**11 <u>j=1</u> a_{01} <u>j=</u>0 **a**10 **a**00 <u>i=0</u> ij

FIGURE 2



APPLN. FILING-DATE: MARCH 21, 2001 TITLE: MATRIX MULTIPLICATION IN INTEGROR

PROCESSING SYSTEM

INVENTOR(S): ALI SAZEGARI APPLICATION SERIAL NO: UNASSIGNED

SHEET 3 of 15

426 428 424 430 c_{23} j=3 c_{13} c_{03} c_{33} j=2 \mathbf{c}_{02} C₁₂ C_{22} c_{32} 1 c_{11} c_{01} ΞĪ c_{21} c_{31} j=0 . 000 c_{10} \mathbf{c}_{20} \mathbf{c}_{30} <u>i=</u>0 i=2<u>i</u>=3 Ξ. 420 == 418 422 \mathbf{b}_{03} \mathbf{b}_{13} b₂₃ j=3 \mathbf{b}_{33} \mathbf{b}_{02} j=2 b_{12} \mathbf{b}_{22} **b**₃₂ 闰 \mathbf{b}_{21} \mathbf{b}_{11} j=1 \mathbf{b}_{01} b_{31} <u>j=</u>0 **. b**00 \mathbf{b}_{10} b_{20} **b**30 <u>:</u> <u>:</u> <u>1</u>=2 <u>l=3</u> 412 X 410 408 414 j=3 \mathbf{a}_{03} \mathbf{a}_{13} \mathbf{a}_{23} a_{33} j=2 a₃₂ a_{02} a_{12} a_{22} j≕l \mathbf{a}_{11} a_{01} a_{21} a_{31} j=0 a_{30} a_{00} \mathbf{a}_{10} \mathbf{a}_{20} <u>i=0</u> <u>1=2</u> <u>i=3</u> i=1

TITLE: NEXT MULTIPLICATION IN A VECTOR

PROCES G SYSTEM

INVENTOR(S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED

SHEET 4 of 15

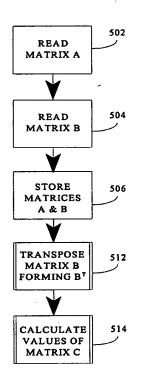


FIGURE 5

TITLE: MATRIX MULTIPLICATION IN A VECTOR

PROCE G SYSTEM INVENTOR(S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED

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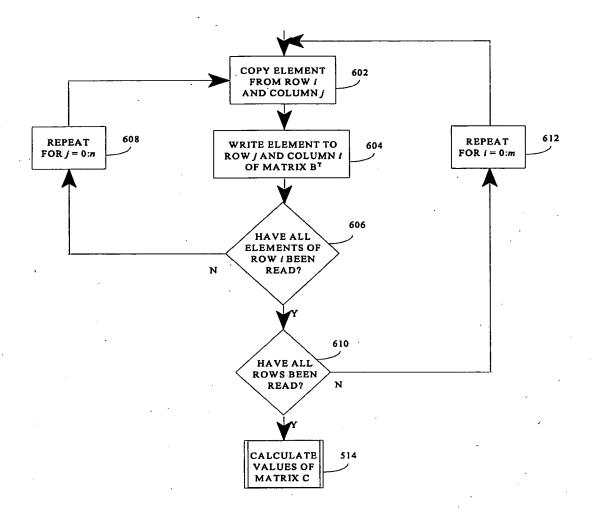


FIGURE 6

TITE MATRIX MULTIPLICATION IN A VEC

PR SSING SYSTEM INVENTOR(S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED

SHEET 6 of 15

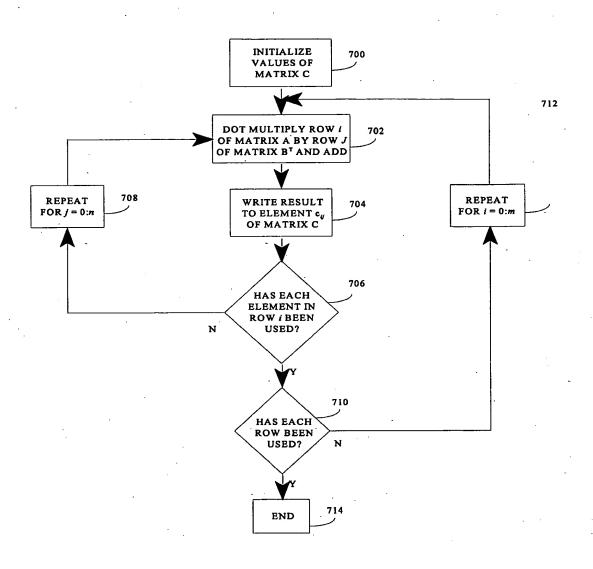


FIGURE 7

THE MATRIX MULTIPLICATION IN A VECT

PROCESSING SYSTEM

Inventor(s): Ali Sazegari

APPLICATION SERIAL NO: UNASSIGNED

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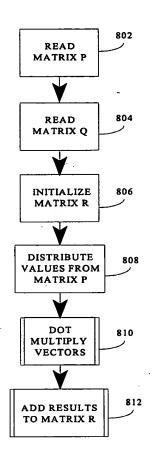


FIGURE 8A

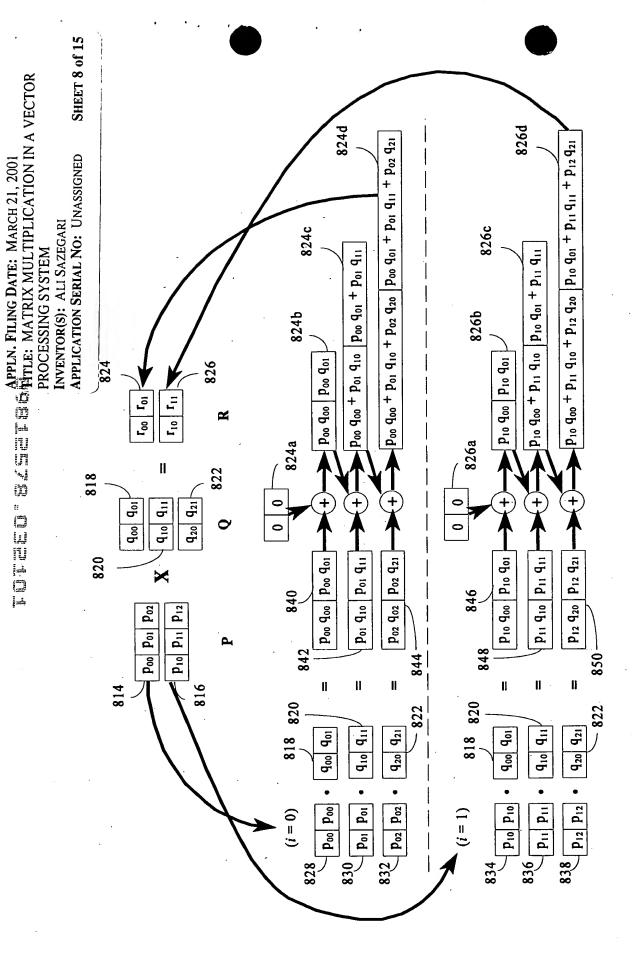


FIGURE 8B

TITLE: MATRIX MULTIPLICATION IN A VECTOR

PROCE NG SYSTEM 'INVEN' S): ALI SAZEGARI

APPLICATION SERIAL NO: UNASSIGNED

SHEET 9 of 15

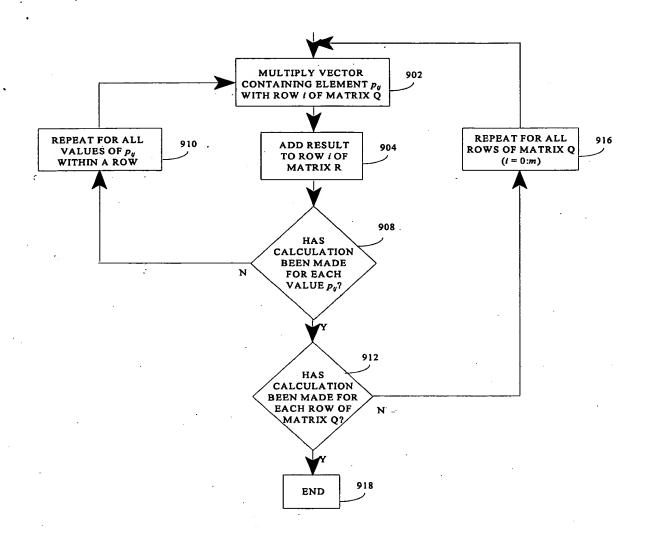
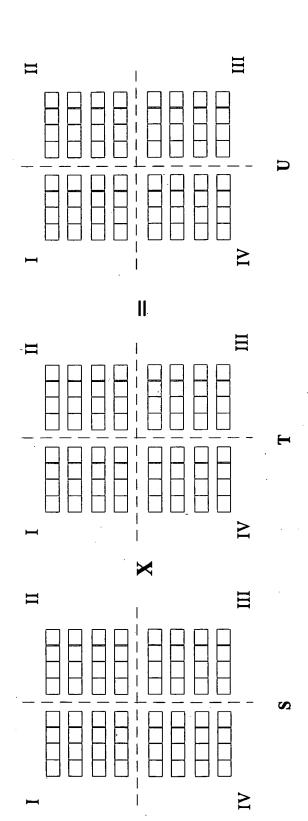


FIGURE 9

APPLN. FILING DATE: MARCH 21, 2001 TITLE: MATRIX MCLETIFL I CATION IN INTEGROR PROCESSING SYSTEM

SHEET 10 of 15 INVENTOR(S): ALI SAZEGARI APPLICATION SERIAL NO: UNASSIGNED



1142 1140 1144 1138 \coprod Z_{23} Z_{33} Z_{03} Z_{13} Z₀₂ Z₁₂ | Z_{22} 7 \mathbf{Z}_{11} \mathbf{Z}_{21} Z_{31} Z_{01} Z₂₀ Z00 \mathbf{Z}_{10} 1134 1148 1146 1136 \geq SHEET 11 of 15 II 1126 1124 1128 1122 III y₃₂ y₃₃ y22 y23 y₀₂ y₀₃ y12 y13 APPLICATION SERIAL NO: UNASSIGNED y20 y21 y₃₀ y₃₁ y10 y111 y₀₀ y₀₁ 1132 1120 1110 1108 1106 1116 III X₃₃ X₀₂ X₀₃ X₁₂ X₁₃ X32 X₂₀ X₂₁ X₃₀ X₃₁ X10 X11 X₀₀ X₀₁ 1102 1104 \geq

APPLN. FILING DATE: MARCH 21, 2001. TITLE: MATRIX MULTIPLICATION IN A VECTOR

INVENTOR(S): ALI SAZEGARI

PROCESSING SYSTEM

IGURE 11

APPLN. FILING DAILS: MARCH 21, 2001 CONTROL TITLE: MATRIX MULTIPLICATION IN A VECTOR PROCESSING SYSTEM INVENTOR(S): ALI SAZEGARI APPLICATION SERIAL NO: UNASSIGNED SHEET 12 of 15

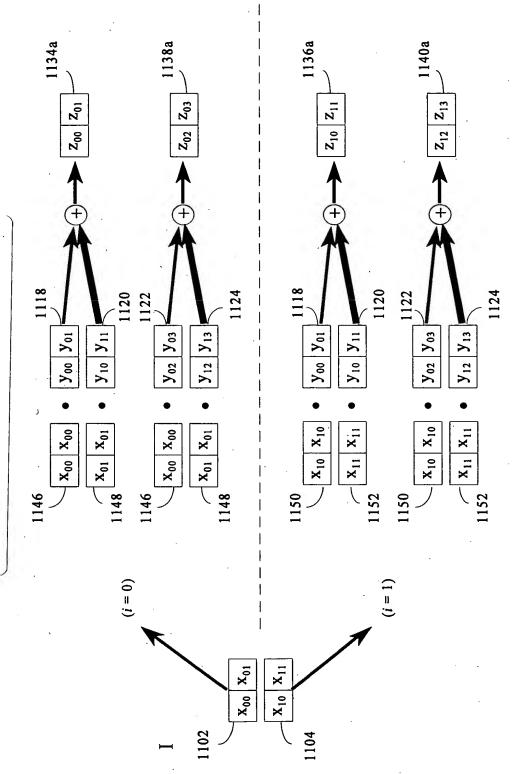


FIGURE 11A

1140b 1134b 1138b 1136b SHEET 13 of 15 TITLE: MATRIXIMULEIRIGEATION IN A YEGTOR PROCESSING SYSTEM 1130 1128 1132 1126 1132 1126 y30 y31 y20 y21 y₃₂ y₃₃ y₃₀ y₃₁ y22 y23 y22 y23 y₃₂ y₃₃ y20 | y21 INVENTOR(S): ALI SAZEGARI APPLICATION SERIAL NO: UNASSIGNED APPLN. FILING DATE: MARCH 21, 2001 X₁₃ X₁₃ X₀₃ X₀₃ $X_{12} \mid X_{12}$ X₁₂ X₁₂ X₁₃ X₁₃ X₀₃ X₀₃ X₀₂ X₀₂ 1156 1160 1158 1154 X₀₃ X₁₂ X₁₃ 1106

FIGURE 11B

1128

 $Z_{30} | Z_{31}$ SHEET 14 of 15 APPLN. FILING DATE: MARCH 21, 2001 TITLE: MATRIX MULTIPLICATION IN ALVECTORIN PROCESSING SYSTEM 1130 1130 1132 1128 1126 1132 y₃₂ | y₃₃ y₃₀ | y₃₁ y22 y23 y₃₀ y₃₁ y20 | y21 y20 | y21 APPLICATION SERIAL NO: UNASSIGNED INVENTOR(S): ALI SAZEGARI X₃₂ X₃₂ X₃₃ X₃₃ $\mathbf{X}_{22} \mid \mathbf{X}_{22}$ X₂₃ | X₂₃ X₂₃ | X₂₃ X22 X22 1168. 1164 1162 1162 (i=2)(i=3)X₃₂ X₃₃ X22 X23

III

1110

1142a

1146a

FIGURE 11C

1128

y32 y33

| X₃₃ | X₃₃

1148b

1126

y22 y23

X₃₂ X₃₂

1166

1148a

1142b 1146b 11486 SHEET 15 of 15 TITLE: MATRIX MULTIPLICATION IN A YECTOR PROCESSING SYSTEM 1120 1124 1120 1122 1122 yoo you y₁₀ | y₁₁ y12 y13 y10 y11 y₀₂ y₀₃ yoo you APPLICATION SERIAL NO: UNASSIGNED APPLN. FILING DATE: MARCH 21, 2001 INVENTOR(S): ALI SAZEGARI X₃₁ X₃₁ X₃₀ X₃₀ X₂₀ X₂₀ X₂₁ X₂₁ $X_{21} X_{21}$ X₂₀ X₂₀ 1172 1176 (i = 2)(i = 3)X₃₀ X₃₁

 \geq

1114

FIGURE 11D

1124

y12 y13

X₃₁ X₃₁

y₀₂ y₀₃

X₃₀ X₃₀

1144b